



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Chen *et al.*

Appl. No.: 10/643,957

Filed: August 20, 2003

For: **High Voltage Power Management  
Unit Architecture In CMOS  
Process**

Confirmation No.: 4983

Art Unit: 2838

Examiner: Patel, Rajnikant B.

Atty. Docket: 1875.4170000

**Information Disclosure Statement**

***Mail Stop Amendment***

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Listed on accompanying IDS Forms are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Applicants have listed publication dates on the attached IDS Forms based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent

application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. No statement or fee is required.

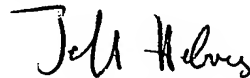
In accordance with 37 C.F.R. § 1.98(a)(2), no copies of U.S. patents and patent application publications cited on the attached IDS Forms are submitted.

It is respectfully requested that the Examiner initial and return a copy of the enclosed IDS Forms, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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Substitute for form 1449/PTO

**Complete if Known****INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Application Number	10/643,957
Filing Date	August 20, 2003
First Named Inventor	Chun-Ying Chen
Art Unit	2838
Examiner Name	Patel, Rajnikant B.
Attorney Docket Number	1875.4170000

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (If Known)			
	US1	2005/0134252 A1	06-23-2005	Chen	
	US2	2005/0168273 A1	08-04-2005	Chen	
	US3	5,410,241	04-25-1995	Cecil	
	US4	5,631,598	05-20-1997	Miranda et al.	
	US5	5,986,910	11-16-1999	Nakatsuka	
	US6	6,049,227	04-11-2000	Goetting et al.	
	US7	6,049,243	04-11-2000	Mihara et al.	
	US8	6,184,716 B1	02-06-2001	Depetro et al.	
	US9	6,370,071 B1	04-09-2002	Lall et al.	
	US10	6,535,019 B2	03-18-2003	De Santis	
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	US12	6,865,382 B2	03-08-2005	Behzad	
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	US14				
	US15				
	US16				
	US17				
	US18				
	US19				
	US20				

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	FP1					
	FP2					
	FP3					
	FP4					
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

Examiner Signature	Date Considered
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